

190

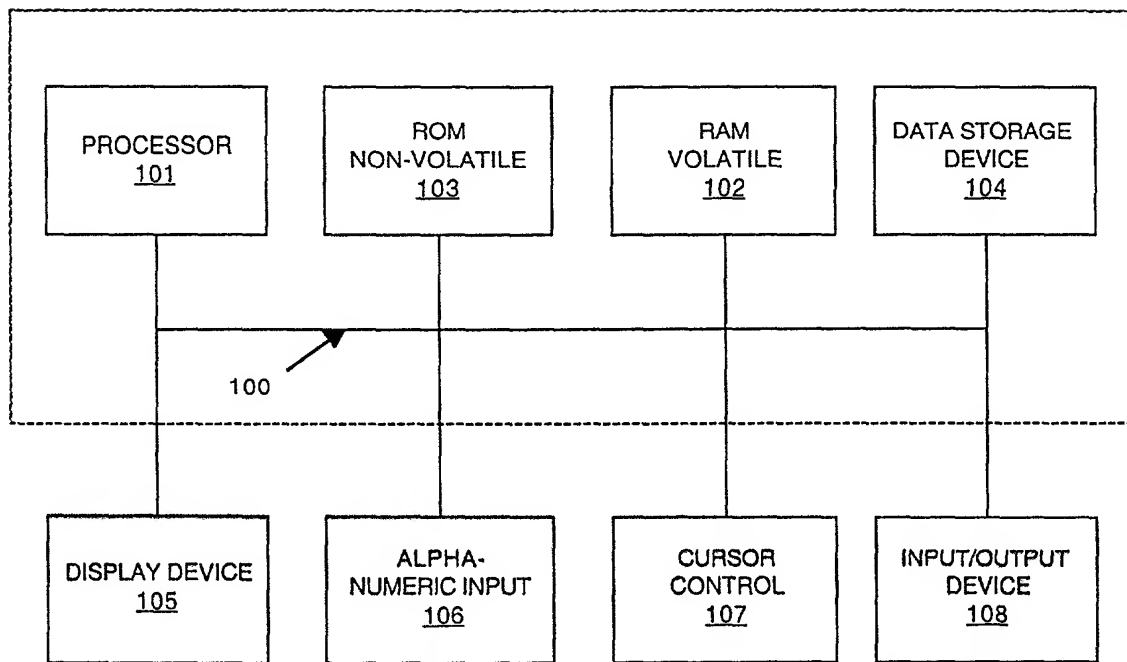


Figure 1

Downloaded from www.scribd.com

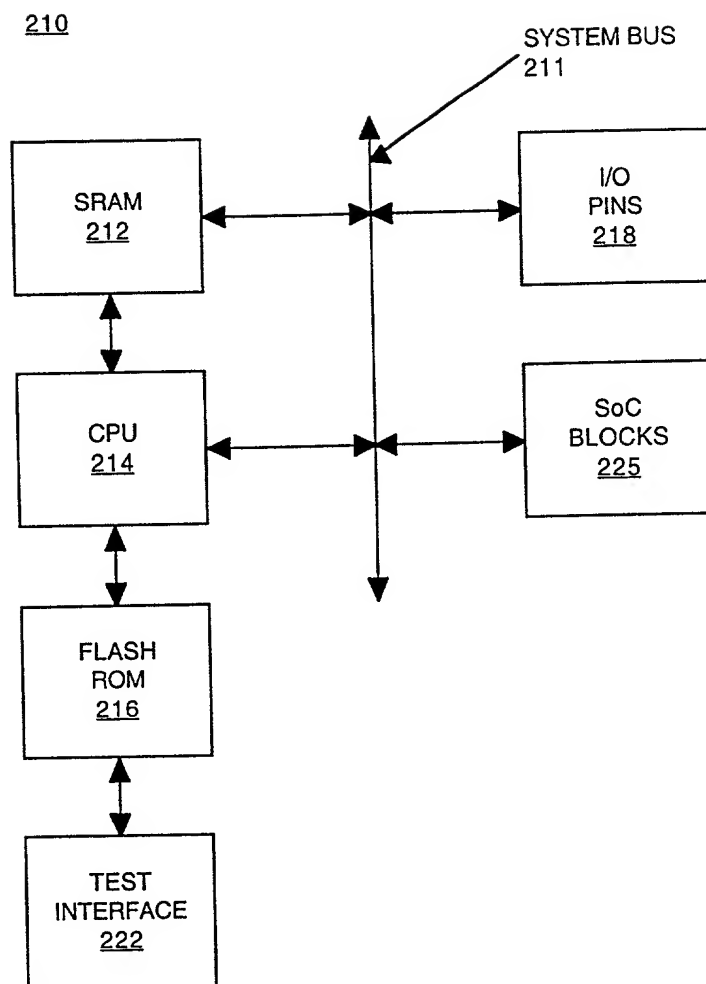


Figure 2A

1003494-1004

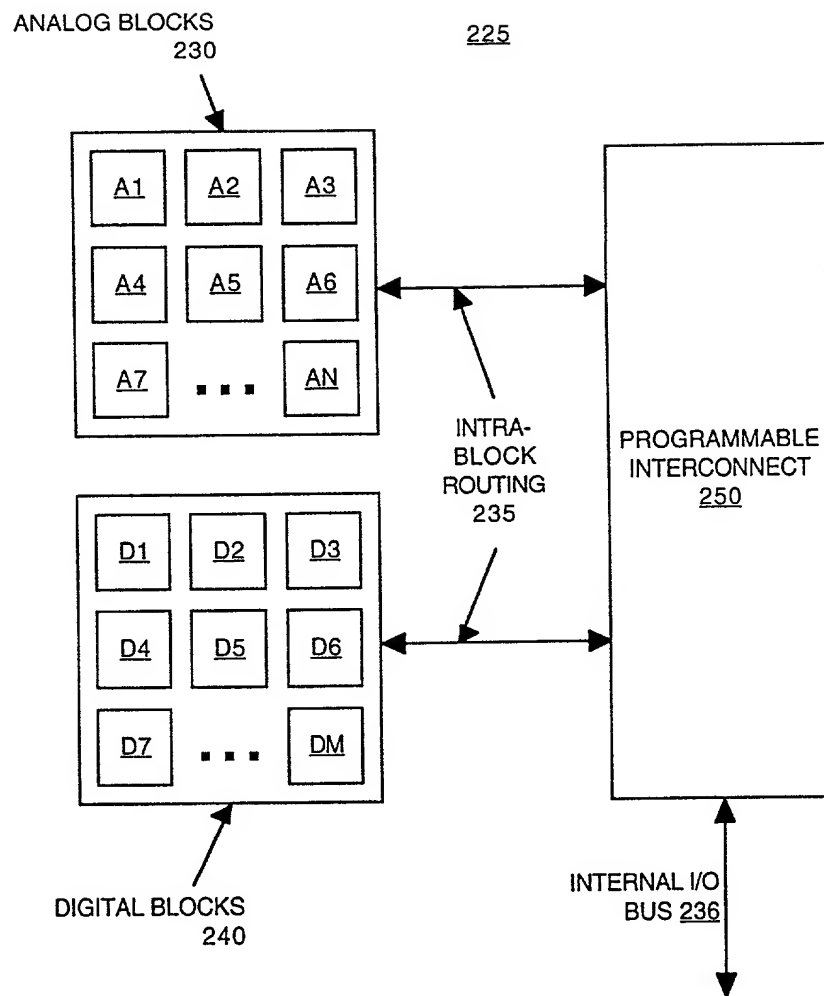


Figure 2B

300

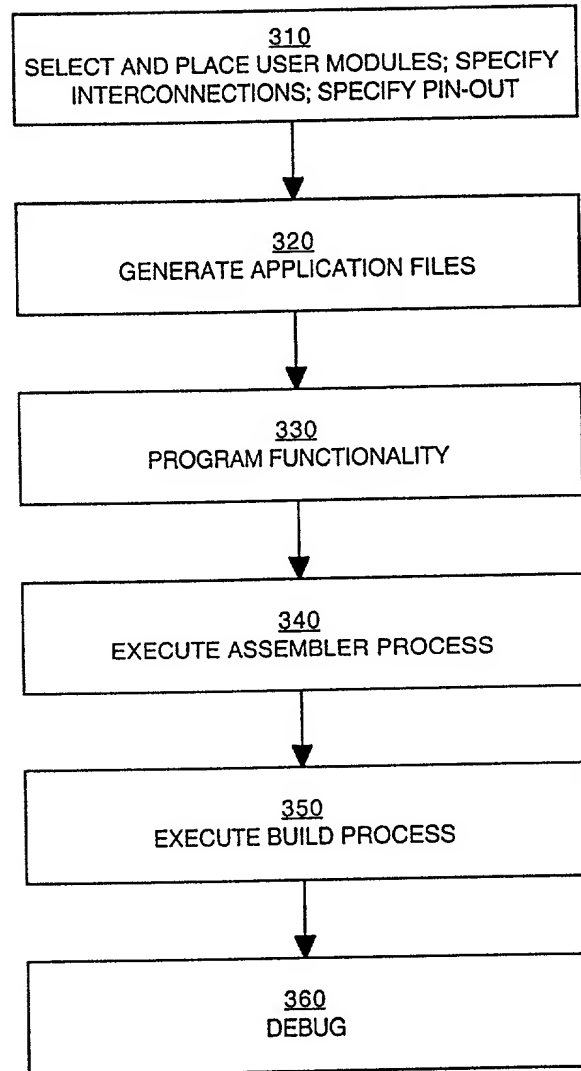


Figure 3

400

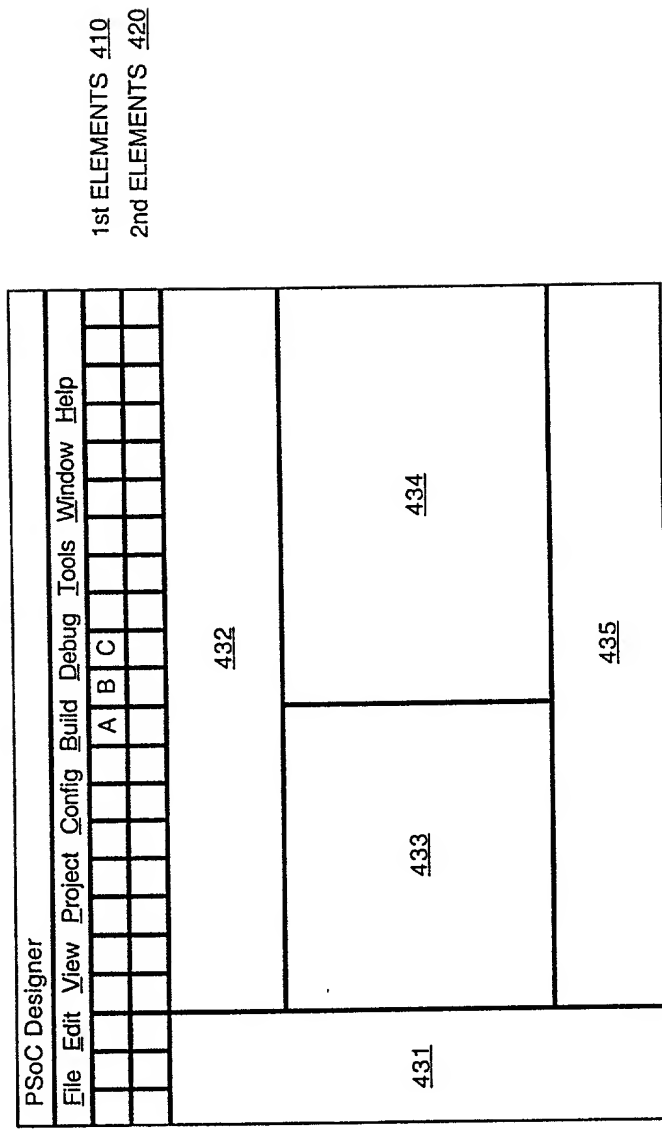


Figure 4A

401

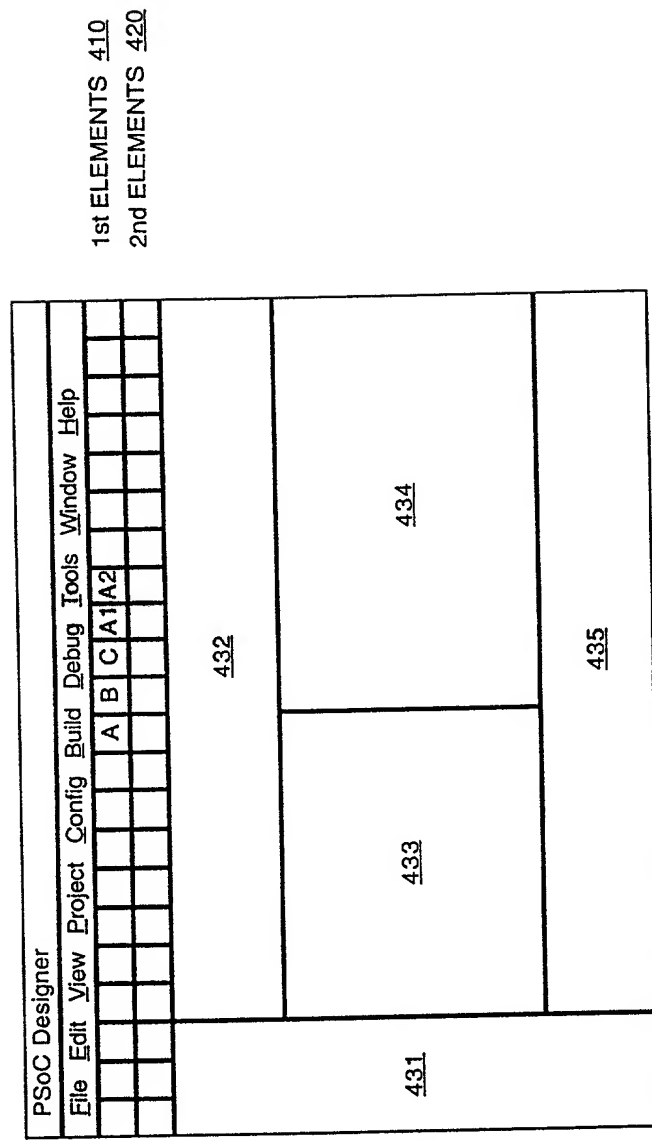


Figure 4B

402

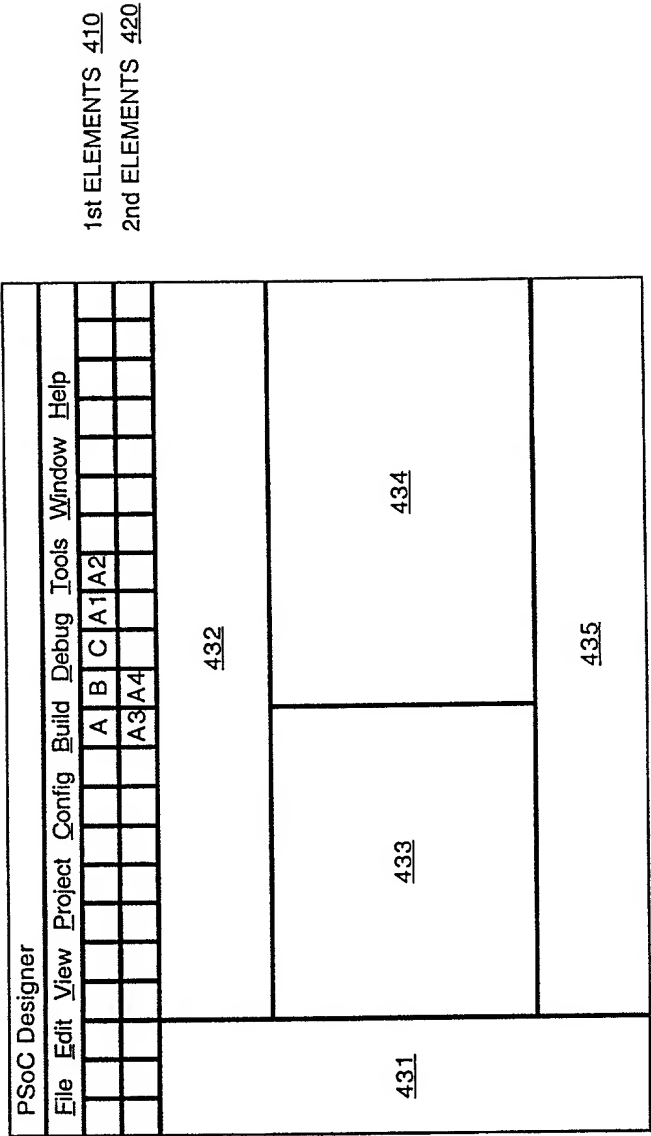


Figure 4C

403

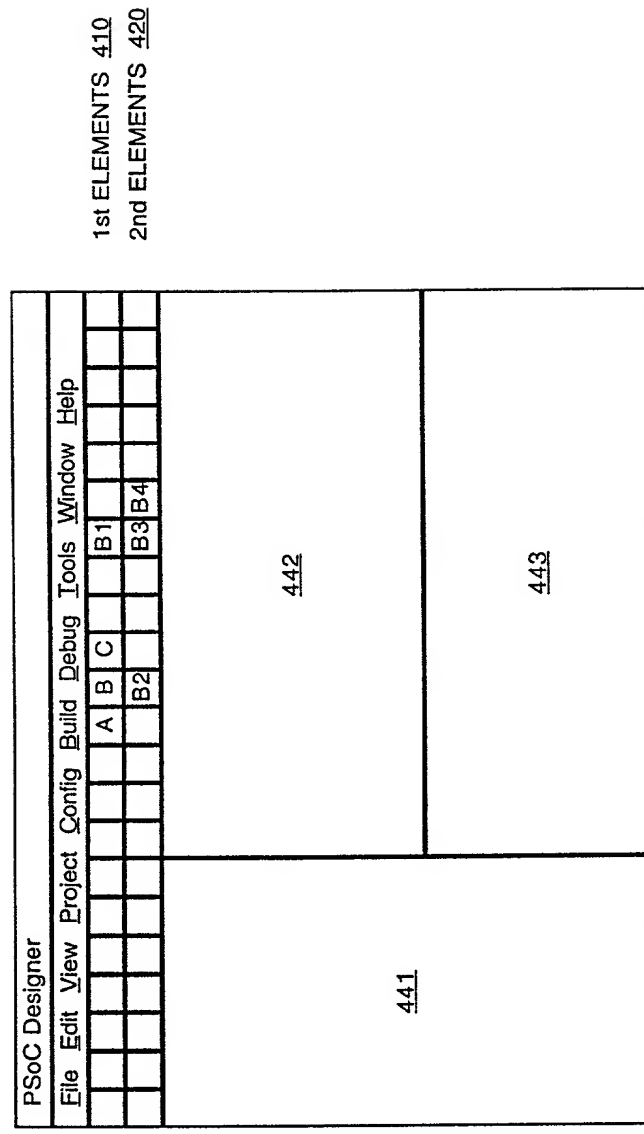


Figure 4D

TOP SECRET

500

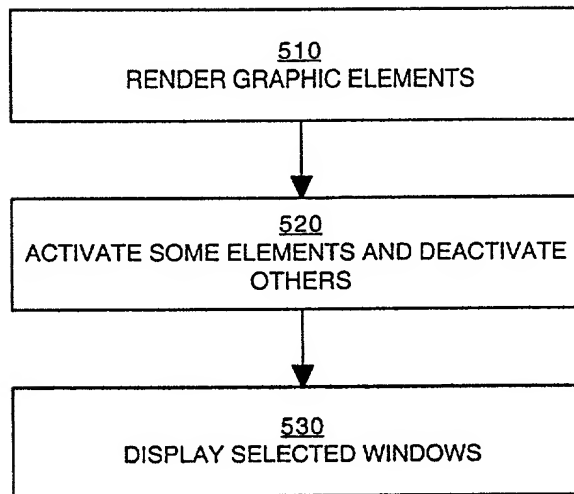


Figure 5

600

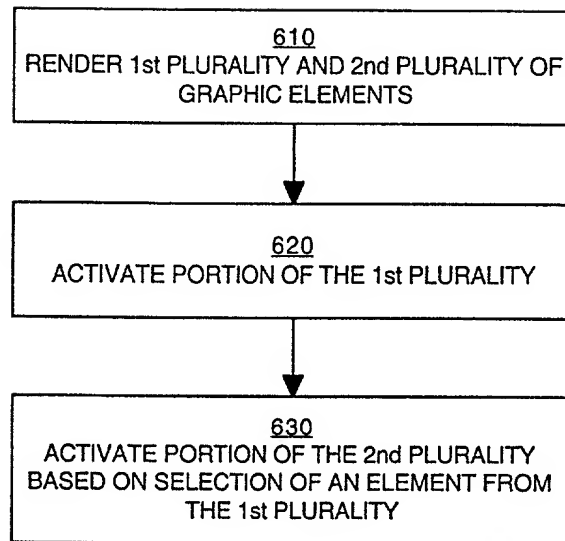


Figure 6

newproject - PSoC Designer - [Device Editor]

File Edit View Project Config Build Debug Program Tools Window Help

Use Modules selected for placement:

AD_CONVERT_1 Counter16_1 DAC8_1 INSAMP_1 INSAMP_2 PWM16_1 UART_1

ADCs
Counters
DACs
Amplifiers
PWMs
PWM8
PWM16
PWMDB8
PWMDB16

Serial User Modules
Timers

Period Register
Counter
Compare Value Register
Clock
Data
Count
TC
Interrupt
Interrupt Type
Output

	Total	Used
Analog Blocks	12	7
Digital Blocks	8	8
RAM	256	6
ROM	16384	674

8-Bit Timer **Timer8** Revision A

Resources:	Required	Optional
PSoC™ Blocks	1 Digital, 0 Analog	
Memory	46 bytes FLASH, 0 bytes SRAM	SRAM in Default Interrupt Routine
Pins		1 per External I/O

Resources Overview Diagram Features Description Specs Options Restrictions API SampleCode Examples ReleaseNotes

For Help, press F1

Start newproject - P... Document1 - Ni... 3:02 PM

Figure 7